

FIG. 1 (Prior Art)

FIG. 2 (Prior Art)

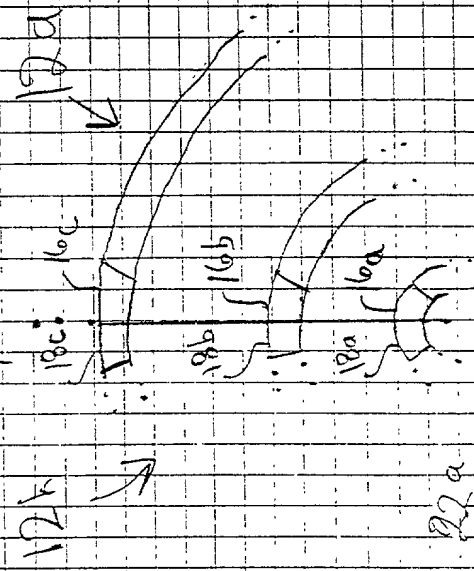


FIG. 3 (Prior Art)

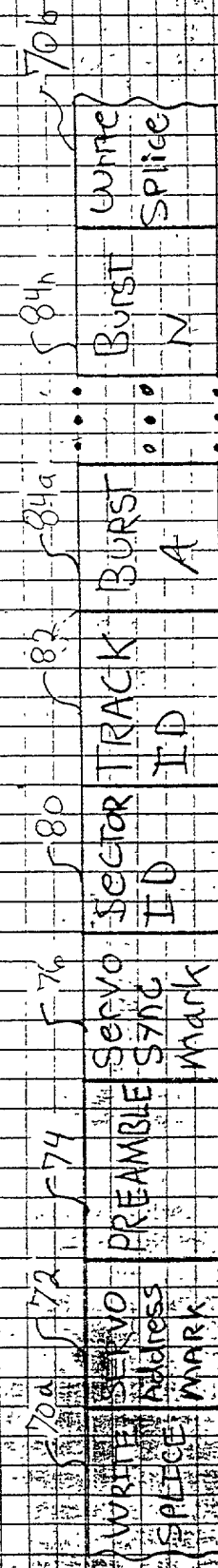


FIG. 6

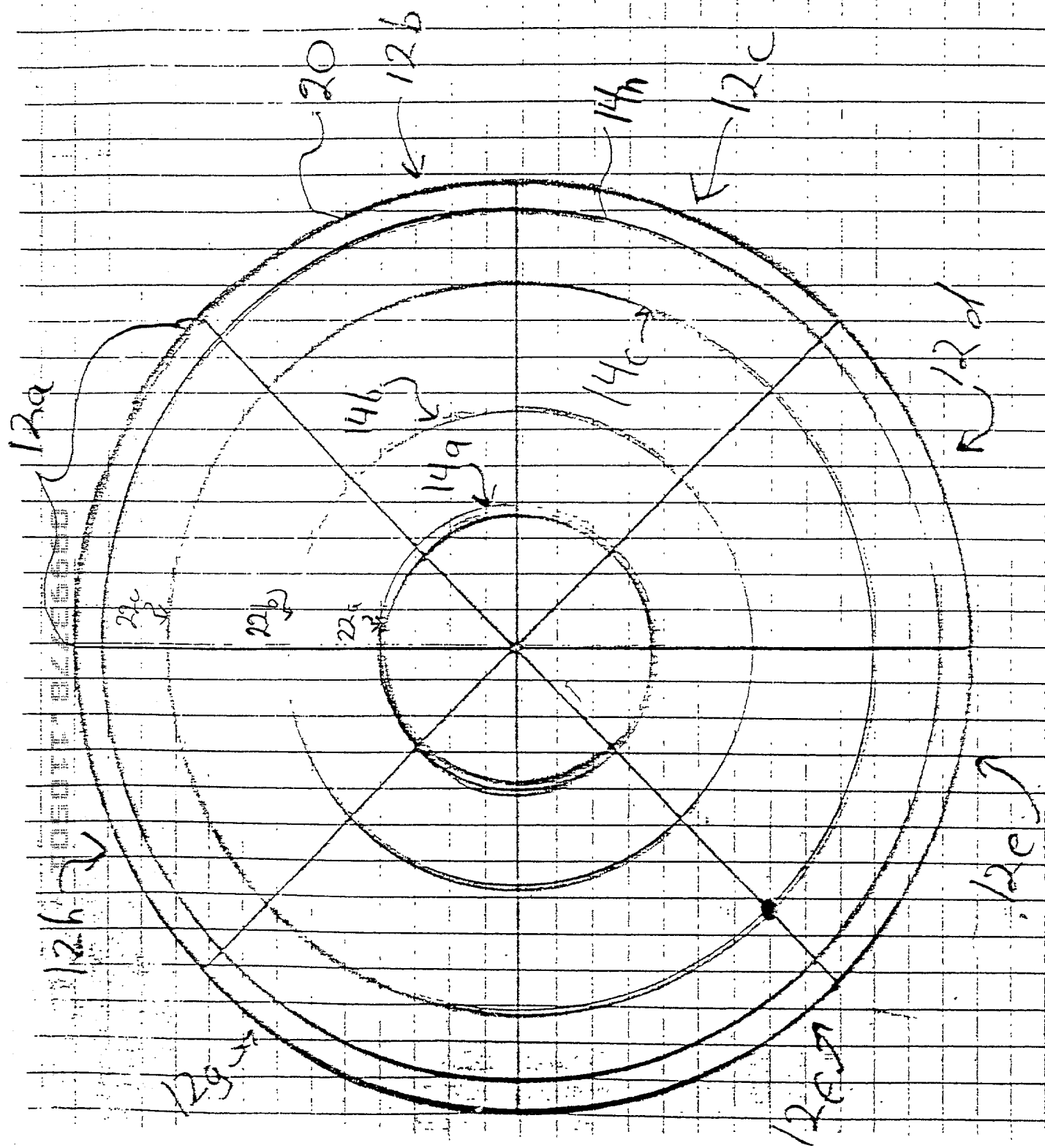


FIG. 4

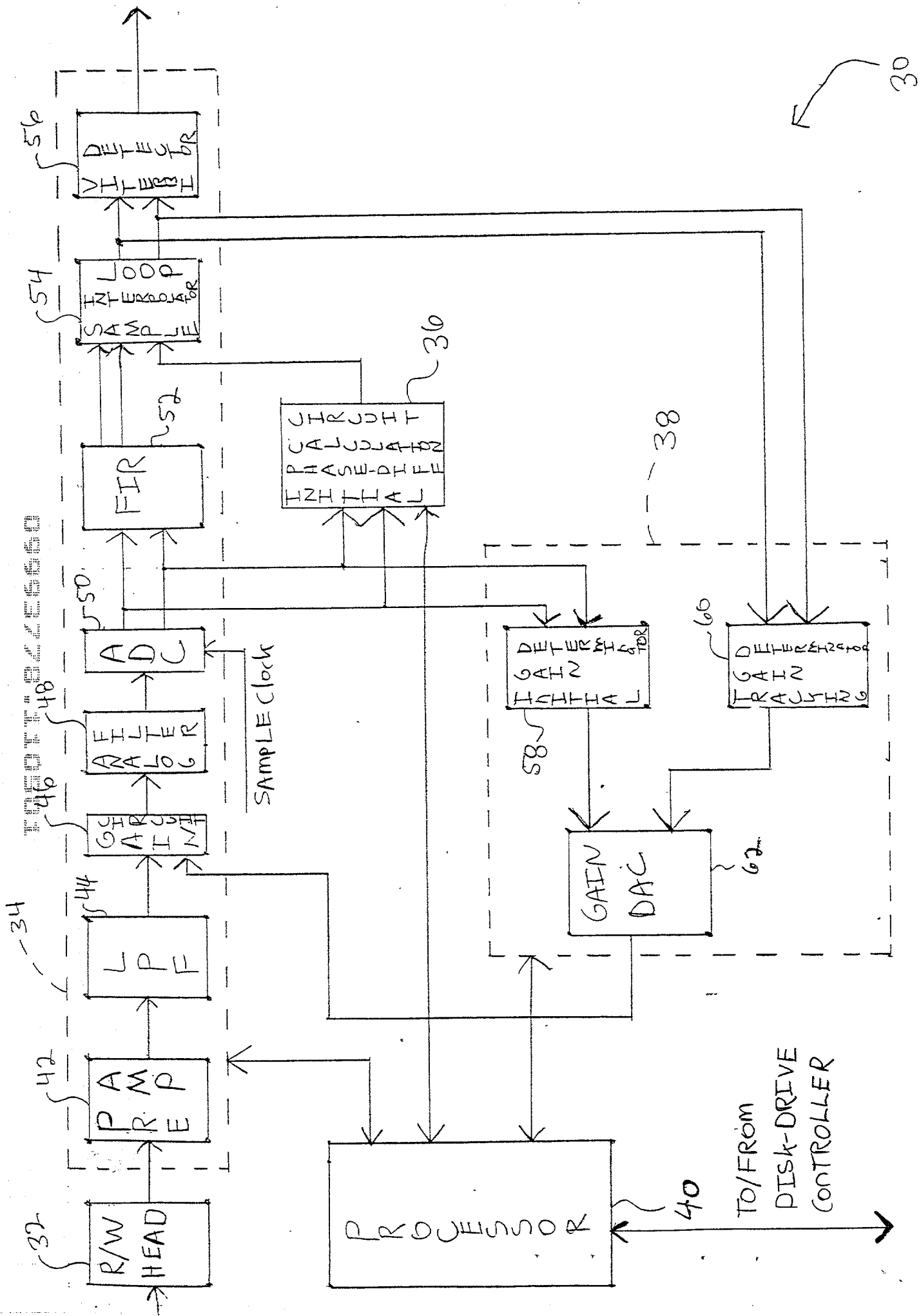
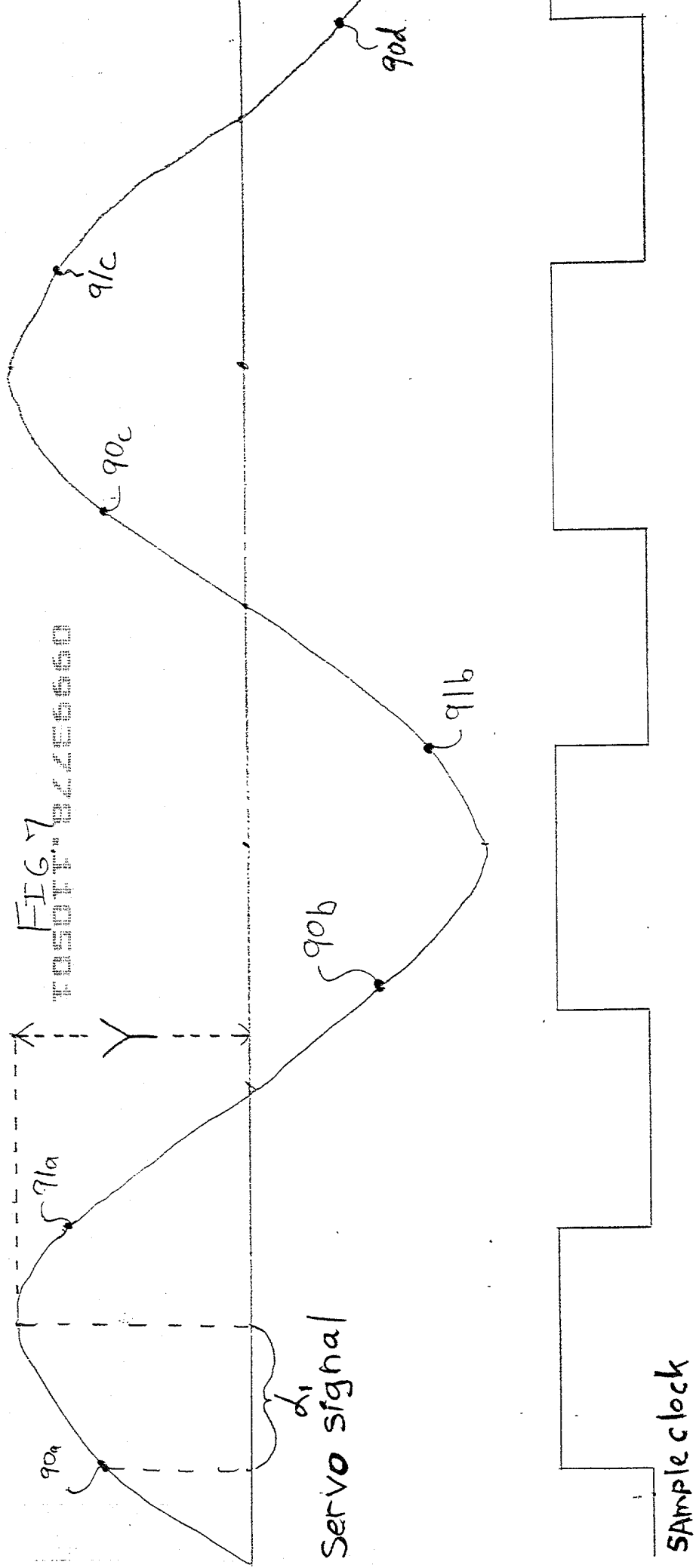


FIG. 5



← PREAMBLE — * — SERVO SYNCHRONIZATION MARK — → Sector ID
 0011001100110000001100000011000011XXXXXX

FIG. 14

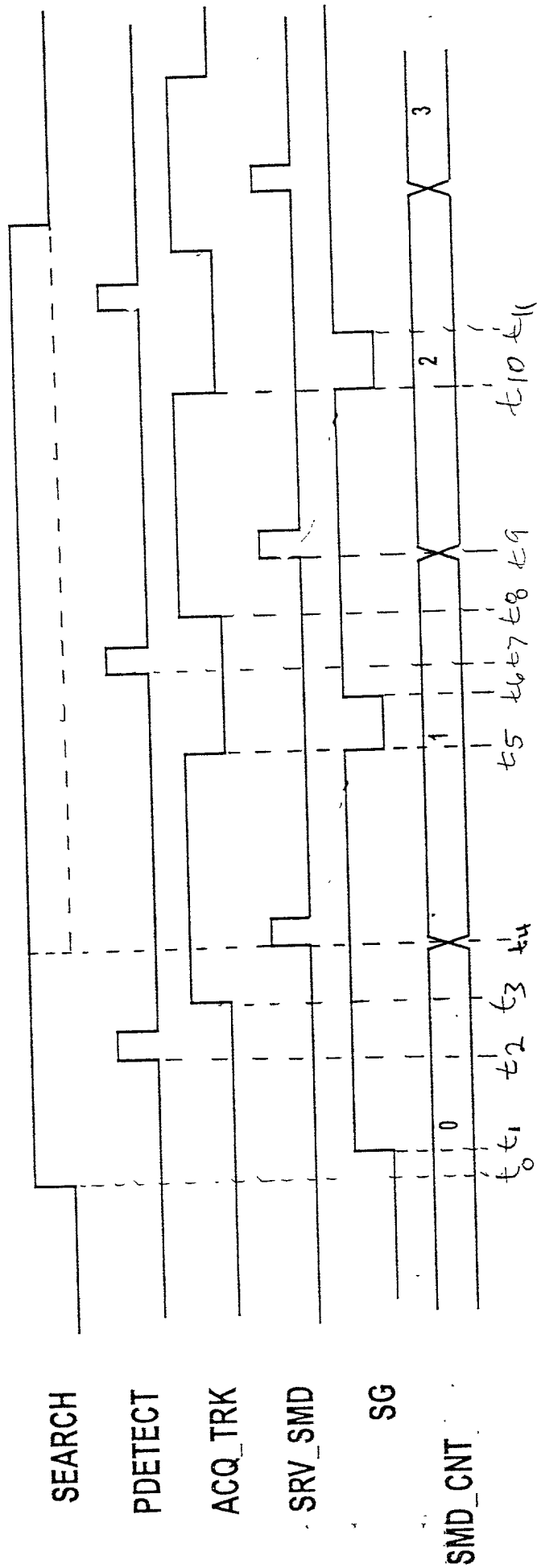
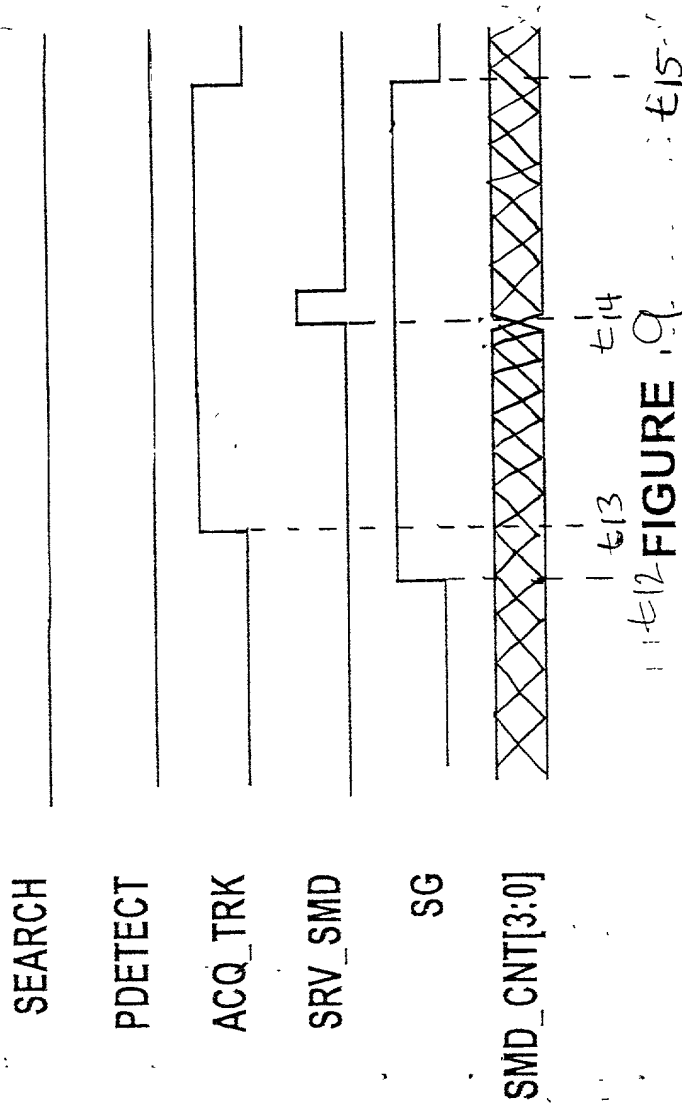


FIGURE 8



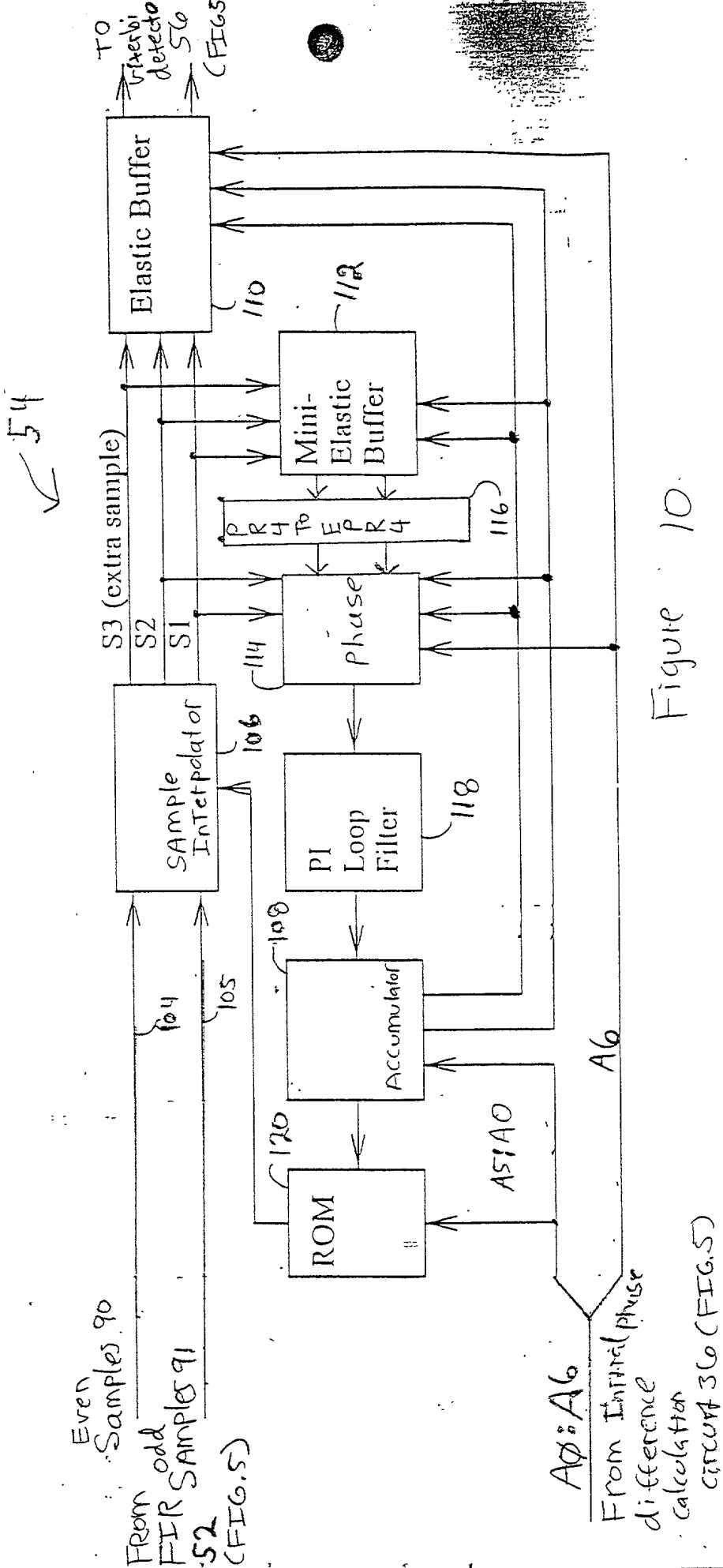


Figure 10

A6: A6
From Initial phase
difference
calculation
circuit 30 (FIG. 5)

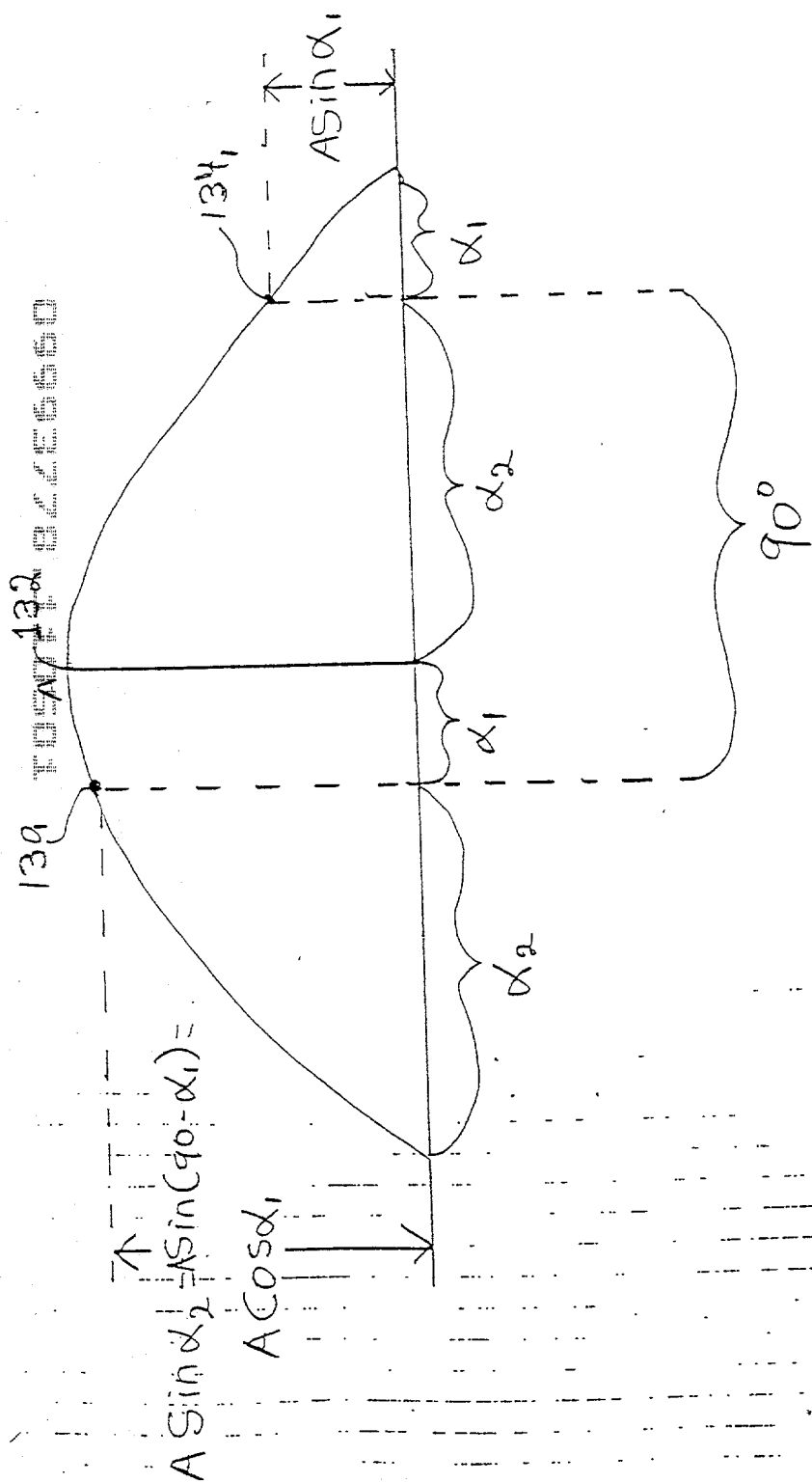


Figure : 11

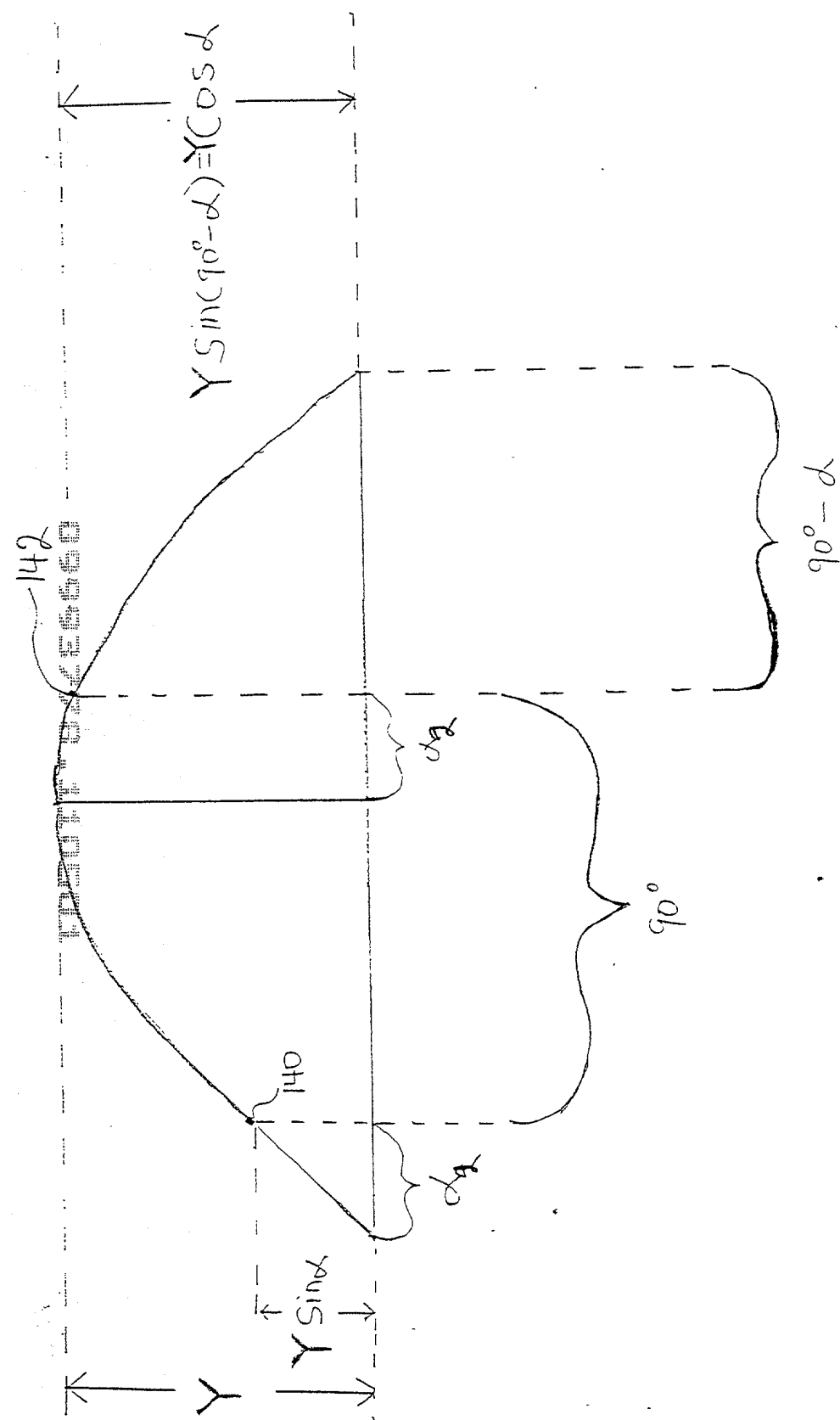
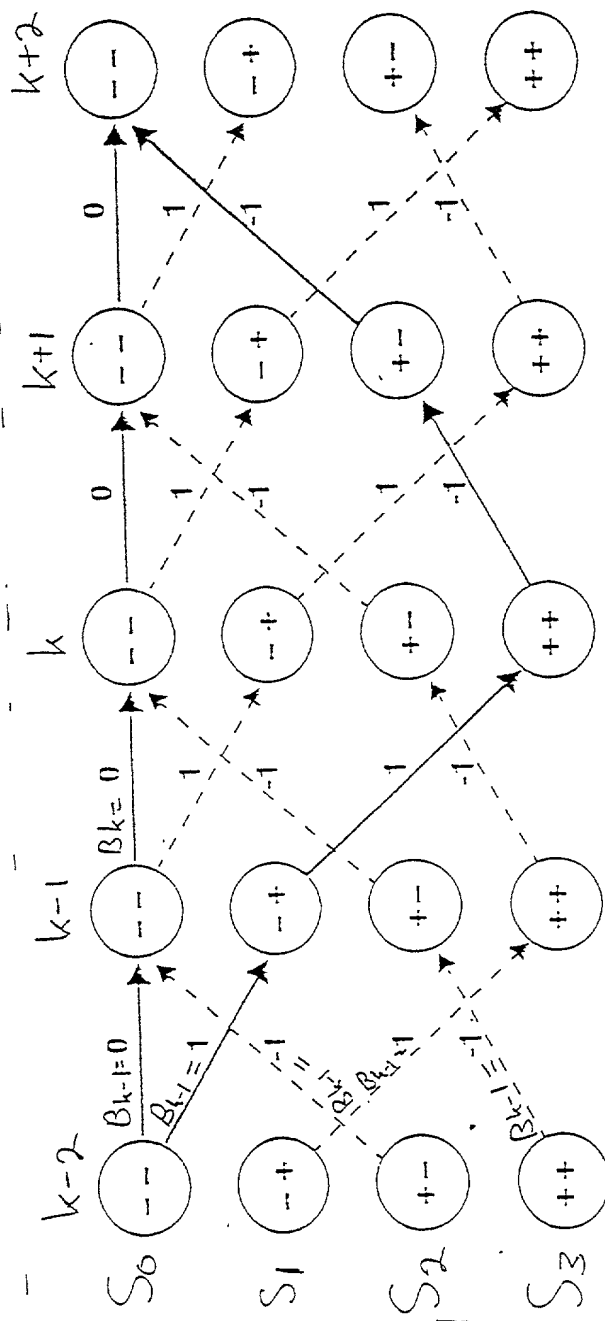


Figure 12

FIGURE 13



154a

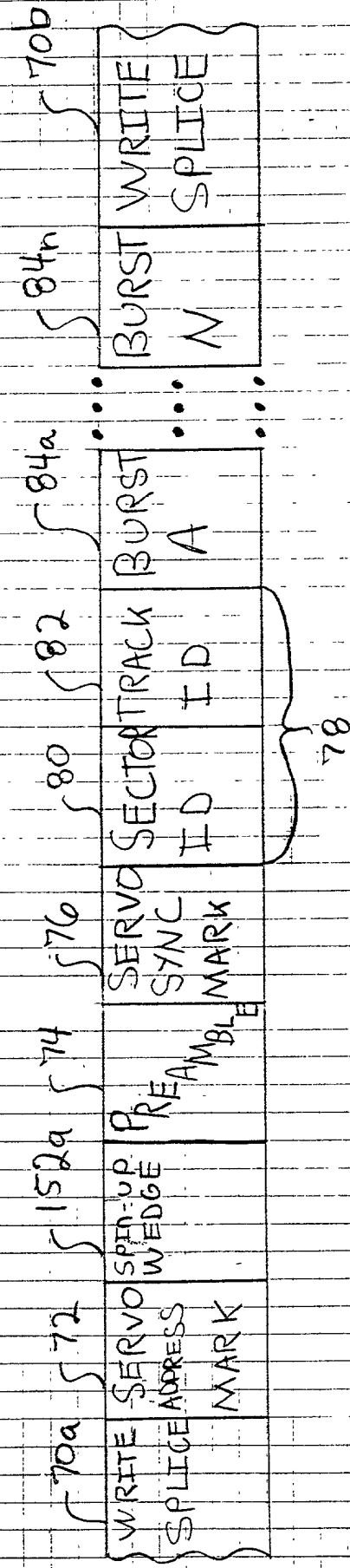


FIG. 16

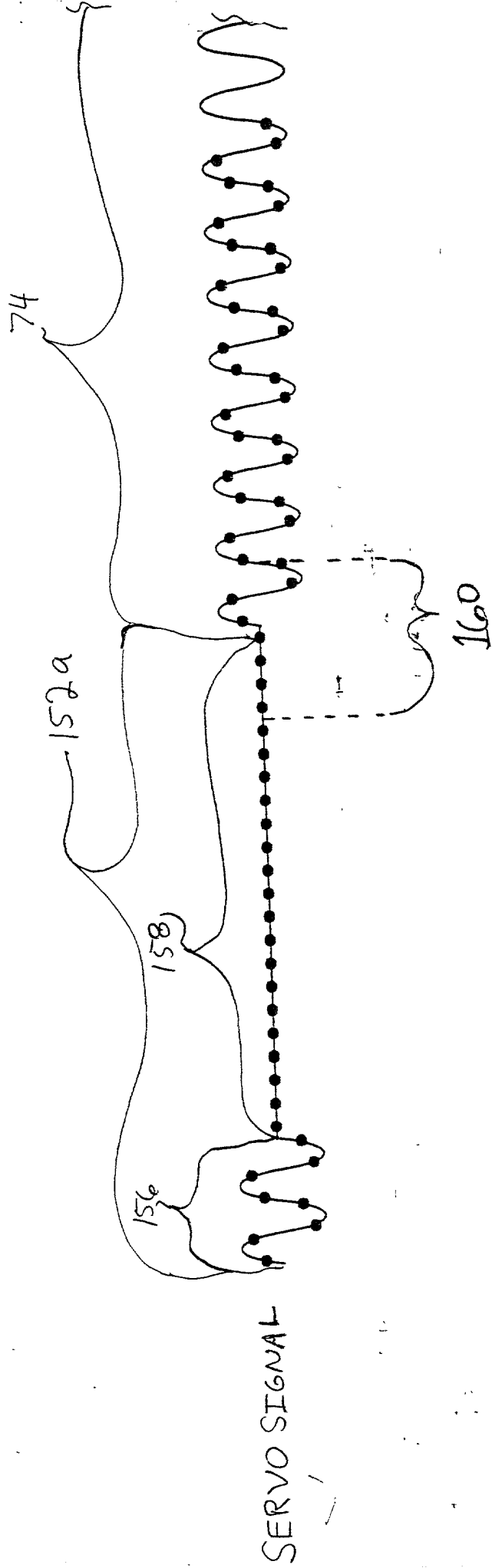


FIG. 17

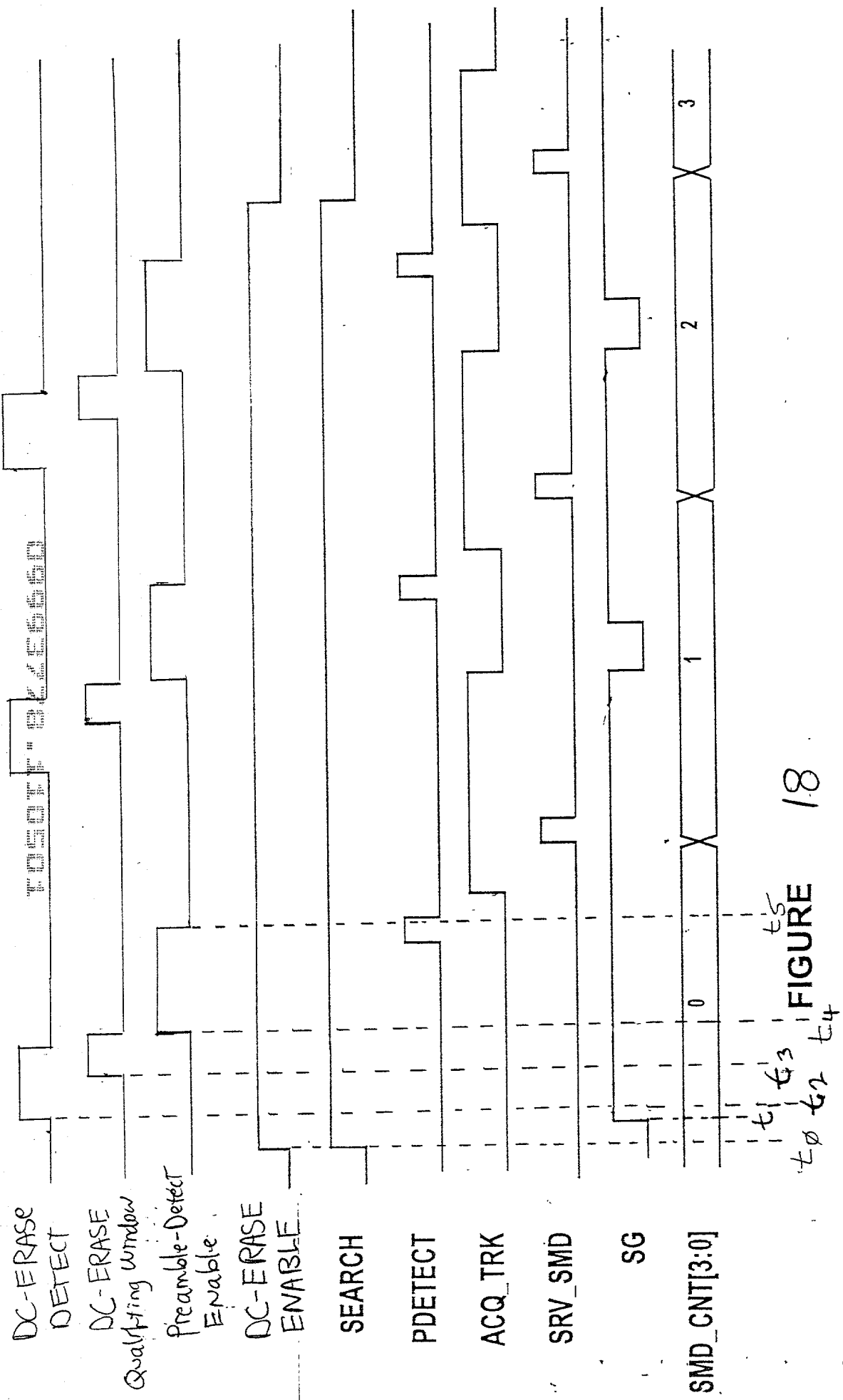


FIGURE 18

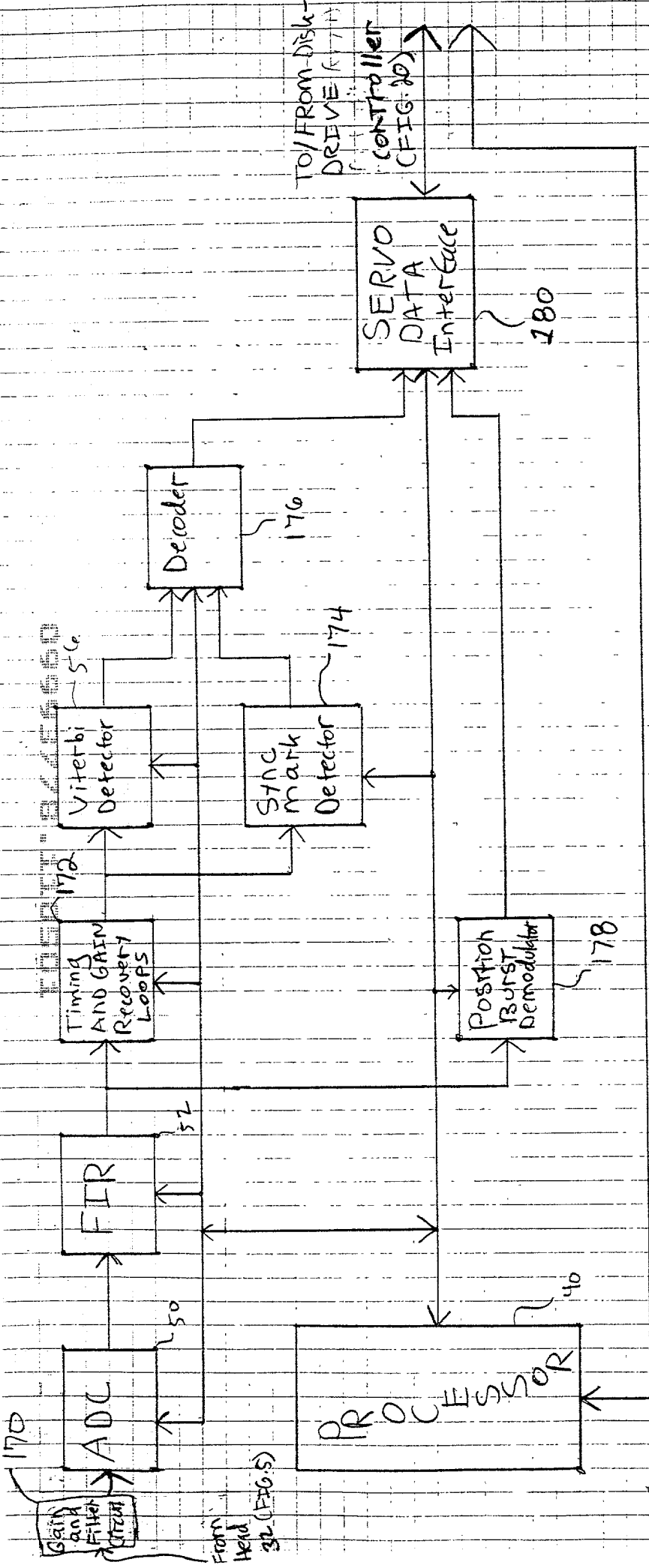


FIG. 19

30

Fig. 20

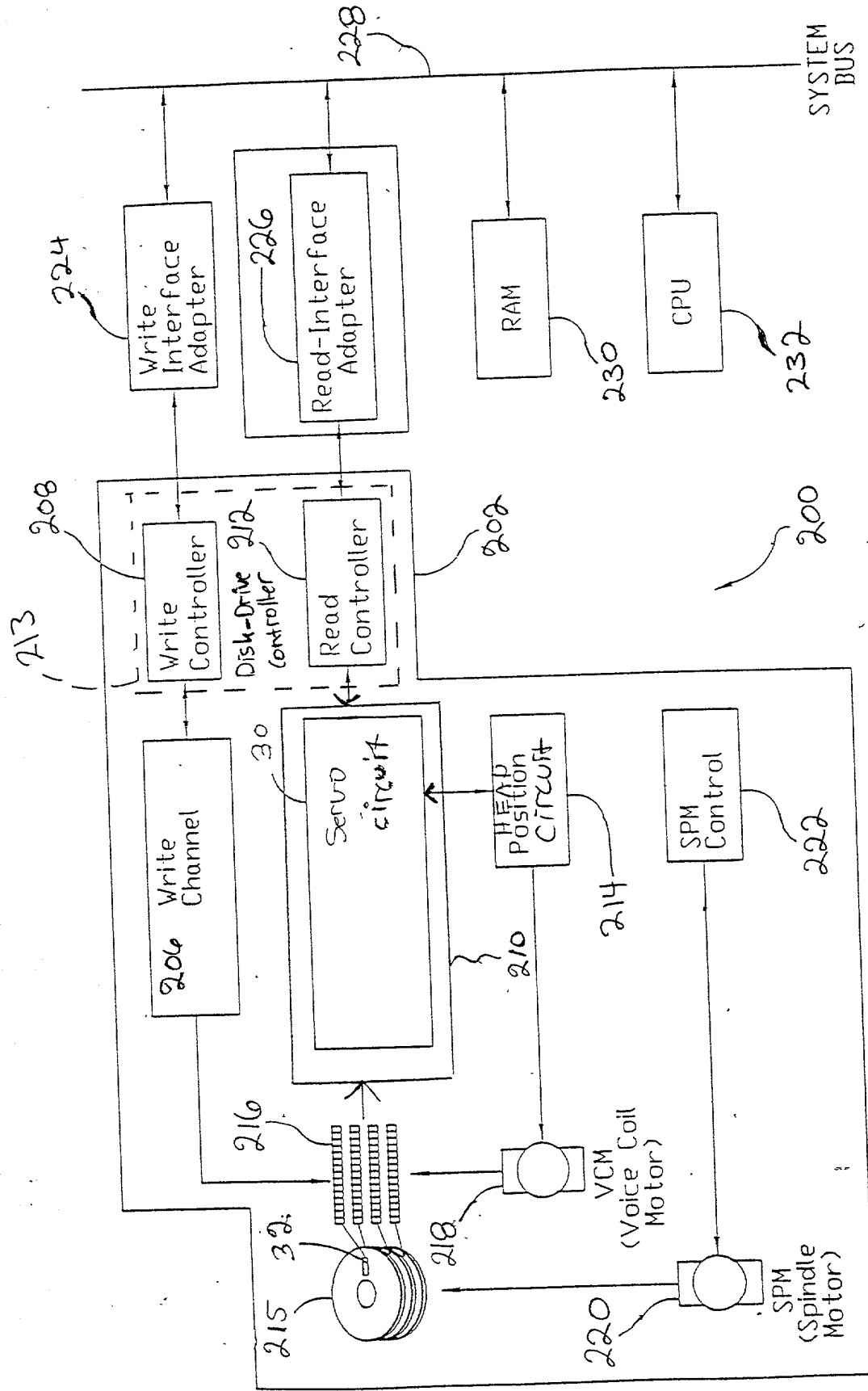


Fig. 20